OpenCL Demo & Multi-FPGA HPC System – HPEC 2015

Visit with BittWare to see how OpenCL can be used to program Altera FPGAs with applications running on BittWare’s Altera Arria® 10 and Stratix® V board platforms. The Open Computing Language (OpenCL™) programming model is supported on Altera’s Arria 10 and Stratix V FPGA architectures and BittWare’s COTS hardware, providing an incredibly powerful solution for system acceleration. The Altera® SDK for OpenCL provides a design environment enabling users to easily implement OpenCL applications on Altera’s FPGAs. BittWare will also be showcasing their TeraBox high performance reconfigurable computing platform which enables multi-FPGA system design for High Performance Computing (HPC) and Network Processing (NP).