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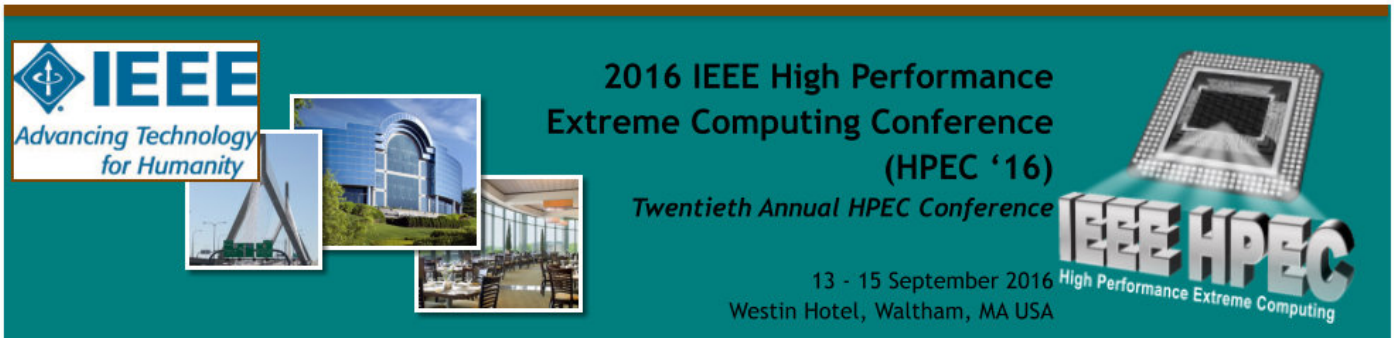

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Call for Papers

download in pdf format

20 May 2016: Paper submission deadline

***The IEEE High Performance Extreme Computing Conference (HPEC '16)* will be held in the Greater Boston Area, Massachusetts, USA on 13 – 15 September 2016. The HPEC charter is to be the premier conference in the world on the confluence of HPC and Embedded Computing.**

Keynote

- Mr. David Martinez (HPEC Founder; IEEE Fellow; MIT Lincoln Laboratory Associate Head Cyber Security & Information Sciences Division) - *HPEC: The Past, Present and Future Outlook*

Invited Speakers

- Mr. Trung Tran (DARPA MTO - Program Manager) - *Machine Learning, Data Analytics, and Non-Conventional Computer Architecture*
- Mr. Robert Bond (MIT Lincoln Laboratory Associate Head ISR Systems & Technology Division) - *Future DoD Computing and the Emergence of Autonomous Systems*
- Dr. Robert Cunningham (Chair IEEE Cybersecurity Initiative; MIT Lincoln Laboratory Group Leader Secure Resilient Systems & Technology) - *End-to-End Security in the Cloud*
- Mr. Clair Grant (Director of R&D VMS Software, Inc) - *OpenVMS: 40 Years of Mission Critical Computing*

Special Events

- BigDAWG Big Data Working Group; organizers: Dr. Tim Mattson (Principal Engineer - Intel) & Dr. Vijay Gadepally (MIT Lincoln Laboratory Supercomputing Center)
- GraphBLAS forum to define standard building blocks for graph algorithms; organizers:Prof. John Gilbert (SIAM Fellow; UC Santa Barbara) & Dr. Scott McMillan (CMU Software Engineering Institute)
- High Performance Data Analysis; organizer: Dr. Jeremy Kepner (MIT Lincoln Laboratory Supercomputing Center)
- Massachusetts High Performance Computing; organizers: Dr. Chris Hill (Principal Research Engineer - MIT) & Prof. Patrick Dreher (North Carolina State)
- Vector, Signal, and Image Processing Libary (VSIPL) standard working group; organizer:Prof. Tony Skjellum (Director - Auburn University Cyber Research Center)
- Health Sciences & HPC; organizers: Dr. Siddharth Samsi (MIT Lincoln Laboratory Supercomputing Center) & Dr. Laura Brattain (MIT Lincoln Laboratory)
- Tools for Quantum Computing; organizers: Mr. Steve Reinhardt (D-Wave Systems) Dr. John Cortese (MIT Lincoln Laboratory)
- RISC-V; organizer: Mr. Kurt Keville (MIT ISN)

Tutorials

- Key Management for Embedded Systems; organizer: Dr. Michael Vai (MIT Lincoln Laboratory Secure Resilient Systems & Technology)

The technical committee seeks new presentations that clearly describe advances in high performance extreme computing technologies, emphasizing one or more of the following topics:

• Advanced Multicore Software Technologies • Case Studies and Benchmarking of Applications • Automated Design Tools • Mapping and Scheduling of Parallel and Real-Time Applications • Computing Technologies for Challenging Form Factors • ASIC and FPGA Advances • Open System Architectures • Data Intensive Computing	• Big Data and Distributed Computing • Interactive and Real-Time Supercomputing • Graph Analytics and Network Science • Fault-Tolerant Computing • Embedded Cloud Computing • Digital Front Ends • General Purpose GPU Computing • Advanced Processor Architectures • Secure Computing & Anti-Tamper Technologies • New Application Frontiers

All submissions will be peer-reviewed and judged on their technical merit, novelty, and relevance to the technical program. Preference will be given to papers with strong, quantitative results. The conference will have 30 minute oral presentations and poster displays. Presenters who wish to have hardware demonstrations are encouraged to mark their preference for a poster display.

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Paper submissions can be up to 6 pages and should use the approved IEEE templates. The highest scoring submissions will be published by IEEE in the official HPEC proceedings available on IEEE eXplore. All other accepted submissions are published on ieee-hpec.org.

Instructions for submitting are posted on the conference web site at: http://ieee-hpec.org/submit.htm

Important Dates:

20 May 2016: Paper submission deadline
17 June 2016: Author notification of acceptance

Vendor Demo Night

Vendors are encouraged to sign up for the Vendor Demo Night. This will allow vendors to present their HPEC technologies in an interactive atmosphere suitable for product demonstration and promotion. Demonstration submissions will be accepted on a space-available basis. Accepted demos will have their title and abstract posted on the ieee-hpec.org web site. A table, three chairs, and a standard outlet will be provided in the demo area. To submit a demo, please fill out and submit the demo form located at: http://ieee-hpec.org/submitdemo.htm

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